

Endorsing AEAD and CTR modes for Keccak

NIST's pending update of FIPS 202 and revision of NIST SP 800-185

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Abstract. We support NIST's potential plan to specify SHA-3 derived functions ("Keccak Modes") for Authenticated Encryption with Associated Data (AEAD). We offer security and performance arguments for a Keccak-based AEAD as an excellent backup and a way to overcome the limitations of AES-GCM, the main current NIST-specified AEAD. We also suggest standardizing parallelizable counter modes for Keccak and allowing their use for encryption, and in DRBGs, MACs, and XOFs.

Keywords: Keccak · FIPS 202 · NIST SP 800-185 · AEAD · Counter Mode

Motivation

This note is a response to NIST's notice¹ about additional SHA-3 derived functions, dated September 4, 2024.

The SHA-3 and SHAKE functions defined in FIPS 202 [26] and the derived functions in NIST SP 800-185 [22] are all based on the 1600-bit keyless permutation $\text{KECCAK-}p[1600, 24]$. As noted in FIPS 202 itself, these functions "[...] can be considered as modes of operation (modes) of the $\text{KECCAK-}p[1600, 24]$ permutation." Hence we call these potential new SHA-3 derived functions simply as "modes".

Background: Why investing in Keccak hardware now makes sense. The main Post-Quantum Cryptography standards ML-KEM [29] and ML-DSA [28] make extensive use of SHA-3 standards, especially SHAKE. Current processor-based implementations of ML-KEM and ML-DSA on microcontroller and vector architectures spend well over 50% of their cycles just computing the $\text{KECCAK-}p[1600, 24]$ permutation [21, 39].

A single invocation of $\text{KECCAK-}p[1600, 24]$ requires thousands of cycles to compute on typical embedded and application-class processors, while a straightforward hardware module can accomplish the same task in 24 cycles [33]. As a consequence, the performance of PQC implementations can be almost doubled just by offering dedicated KECCAK acceleration. The acceleration for SHAKE parameter sets of hash-based signature standards SLH-DSA, XMSS, LMS [10, 30] is, of course, even more significant (perhaps 10×.)

Hence, there currently exists a strong motivation for the inclusion of powerful KECCAK acceleration either as a memory-mapped peripheral (for lower-end systems) or as an "all-rounds" instruction in future processor architectures [34]. Note that partial SHA-3 support, such as FEAT_SHA3 instructions in ARM [2], only accelerates a part of a single round, resulting in less significant gains. With increased architectural support, we can expect KECCAK-based AEAD schemes to clearly outperform their AES counterparts, as they do in pure hardware.

¹NIST Proposes to Update FIPS 202, "SHA-3 Standard" and Revise SP 800-185, "SHA-3 Derived Functions". <https://csrc.nist.gov/News/2024/proposal-to-update-fips-202-and-revise-sp-800-185>

NIST Already Uses it Pt. 1: Counter Modes for Keccak

Output generation in SHAKE [26], cSHAKE, and TupleHashXOF [22] is sequential due to state chaining from block N to block $N + 1$. While it is easy to run SHAKE in an *ad hoc* counter mode by simply concatenating a seed with a counter to generate blocks of output (single KECCAK- p [1600, 24] generates 168 bytes in SHAKE128 and 136 bytes for in SHAKE256), the use of such a system for encryption or random bit generation is not presently allowed by NIST. We suggest standardizing counter mode output and explicitly allowing it to be used for encryption (analogous to AES-CTR [14]), Deterministic Random Bit Generation [3], XOFs, and MACs [22]. For key-derivation functions (KDFs), NIST already describes Keccak-based counter mode [9] – however this KMAC/cSHAKE-based mode seems to require some KECCAK- p [1600, 24] invocations that are not strictly necessary.

We note that lattice-based PQC standards [28, 29] already extensively use the SHAKE XOF for random seed extension and also implicitly describe counter modes for SHAKE. Counters and indices are concatenated with seed inputs for SHAKE-based sampling functions in ML-KEM[29] (SampleNTT, SamplePolyCBD $_{\eta}$) and in ML-DSA[28] (RejNTTPoly, RejBoundedPoly). Data-parallelized KECCAK is used to implement these operations in the original AVX2 code [36, 37], as well as in ARM [4], and RISC-V [39] implementations.

NIST Already Uses it Pt. 2: Permutation AEAD Modes

Despite a solid theoretical framework for using permutations for encryption and Authenticated Encryption with Associated Data (AEAD) that predates the SHA-3 standard itself [1, 7], no such mode is currently offered based on the KECCAK permutation.

In the meantime, NIST has selected [38] the permutation-based ASCON family [13] as the upcoming lightweight cryptography standard. The permutation p in ASCON has many similarities to the KECCAK permutation (and was clearly inspired by it) but is made “lightweight” by being only 320 bits in size, compared to 1600 bits of KECCAK and having a reduced number of rounds. The AEAD mode of ASCON is based on the MONKEYDUPLEX construction [8], which was originally proposed for use with the KECCAK permutation.

In addition to a KECCAK mode that is analogous to the ASCON’s AEAD mode, we suggest standardizing a parallelizable AEAD mode. NIST may also consider abstract APIs that allow “sessions” that simultaneously provide transcripts of communications and allow lightweight full-duplex protocols [19, 32].

Keccak AEADs: A Safe Alternative to AES AEADs

Limitations of AES. Essentially, all AES [27] modes are subject to a $\approx 2^{61}$ -block “birthday bound” for encryption under a given secret key; the wide permutation size of KECCAK allows more long-lived keys. Furthermore, there is sufficient capacity in the KECCAK permutation to accommodate long nonces/IVs together with long sequence numbers. Currently the compromise is often at $\text{nonce} + \text{ctr} = 96 + 32 = 128$ in GCM[16] and CCM[15]. This is one of the reasons why AES-GCM keys are limited to 2^{32} blocks [20, 24].

Keccak seems more secure in the long run. After more than 15 years of intense cryptanalysis, the security margin of KECCAK- p [1600, 24] remains very large. The best relevant attacks apply to at most seven of 24 rounds [18, 23, 35], and halving the number of rounds to 12 would still offer a reasonable security margin [5]. AES, on the other hand, has hardly any security margin left, as is apparent in NIST’s own 2021 review [25].

There has been a suggestion to standardize Rijndael with 256-bit block size [17] to address the limitations of AES. This variant has not been cryptanalyzed much since it was proposed in the late 1990s [12]. It can be argued that the 256-bit Rijndael round function

is in some ways “weaker” than the 128-bit round function used in AES, requiring more rounds to reach the same basic random-indistinguishability properties. “Rijndael-256” is likely to require more rounds in addition to a redesigned key schedule. More research effort would be required to reach the same level of confidence in the security of the redesigned “Rijndael-256” that is already enjoyed by KECCAK- p [1600, 24].

Overall, KECCAK-based AEADs offer higher security guarantees than AES-GCM or other AES-based AEADs. This is true for both confidentiality and integrity protection. The authentication tag produced by MONKEYDUPLEX or similar KECCAK-based AEAD modes maps to its actual security level, which is not the case with GCM beyond 64 bits.

Energy Efficiency, Critical Path Length, and Side-Channel Security. A single Keccak permutation is larger than an AES module but “performs the work” of $136/16 = 8.5$ AES-256 invocations or $168/16 = 10.5$ AES-128 invocations. The finite field multiplication in GCM also requires power. The basic hardware efficiency metrics of KECCAK are superior to most other symmetric schemes, including AES. The critical path of AES is made relatively long and inefficient mainly by the complexity of its S-Boxes [31]. Producing each output bit requires fewer logical operations (gates) with KECCAK modes than even with a 10-round AES-128. These S-Boxes also make the constant-time implementation of AES cumbersome compared to the χ function of KECCAK on “pure” software targets.

Most experts agree that the KECCAK permutation is relatively straightforward to protect against power- and emissions-based side-channel attacks. This was one of the original design considerations of KECCAK [6, 11], and there has been much subsequent work. Both ML-KEM and ML-DSA process secret variables using SHAKE, so a secure hardware module is likely to contain a side-channel secure KECCAK in any case.

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